



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: L03-055

Re: Attorney Docket No. 1054.030

In re application of: Howard Tang, Satwant Singh, and Ann Wu

Serial No.: 10/809,658

Group No.: TBD

Filed: 03/25/04

Examiner: TBD

For: Configuring FPGAs and the Like Using One or More Serial Memory Devices

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 CFR § 1.56, this is an Information Disclosure Statement (IDS) for the above-captioned U.S. patent application. An IDS Form PTO-1449 listing references is enclosed herewith and forms a part hereof.

Certification Under 37 CFR § 1.704(d)

- [] Each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this statement.

Certification Under 37 CFR § 1.97(e)

- [] Each item of information contained in this statement was either (1) cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement or (2) not known to any individual designated in § 1.56(c) more than three months prior to the filing of this statement.

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Certification Under 37 CFR 1.8

Date of Deposit

5/3/04

I hereby certify that this paper and any accompanying papers or fees are being deposited with the U.S. Postal Service with sufficient postage as first class mail under 37 CFR 1.8 on the date indicated above and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Gerry Marrero
(Name of person mailing)

Gerry Marrero
(Signature of person mailing)

Fee Under 37 CFR § 1.97(c)

☐ The fee for submission of this IDS under § 1.97(c) is \$180.00 as set forth in § 1.17(p).

Petition Under 37 CFR § 1.97(d)

☐ This is a petition requesting consideration of an IDS under § 1.97(d). The petition fee for submission of this IDS under § 1.97(d) is \$180.00 as set forth in § 1.17(p).

Provision of Copies of References

☒ A copy of each of the listed references is enclosed herewith and forms a part hereof.

☐ A translation of each of Reference(s) ___ is enclosed herewith and forms a part hereof.

☐ A copy of the International Search Report from a corresponding PCT application is enclosed herewith.

☐ A copy of the European Search Report from a corresponding EPO application is enclosed herewith.

☐ A copy of the final U.S. office action for U.S. patent application no. _____ filed on _____ as attorney docket no. (Case name or our ref.).

☐ The above-captioned U.S. patent application is a

- ☐ continuation
- ☐ divisional
- ☐ continuation-in-part

application of U.S. application serial number ____, filed on ____ as Attorney Docket No. ____.
Each of the listed references was previously cited by or submitted to the Office in the prior application. The above-captioned U.S. patent application relies upon the prior application for an earlier filing date under 35 U.S.C. 120. Pursuant to § 1.98(d), copies of the listed references are not being provided with this IDS.


Authorization To Charge Deposit Account

☒ Authorization is given to charge **Mendelsohn & Associates, P.C. Deposit Account No. 50-0782** for any fee under § 1.97(c) or § 1.97(d).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449 form as confirmation that each of the documents has been considered.

Respectfully submitted,

Date: 5/3/04
Customer No. 22186
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Enclosures

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
REV. 2-32) PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

SERIAL NO.

1054.030
APPLICANT

10/809,658

Howard Tang, Satwant Singh, and Ann Wu	
FILING DATE	GROUP

03/25/04

Not yet assigned

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

[illegible][illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA	Atmel Datasheet entitled "FPGA Configuration EEPROM Memory 3.3V and 5V System Support", Rev. 2321E-CNFG- 06/03, pp. 1-23.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449)